



PATENT
Attorney Docket No.: SAM-0564
Customer No.: 29344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jae-Yoon Yoo, *et al.*
Serial No.: 10/806,301
Filing Date: March 22, 2004
Title: METHOD OF FABRICATING MOS TRANSISTOR USING TOTAL GATE SILICIDATION PROCESS

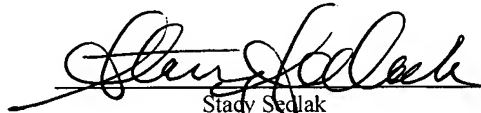
Examiner: Lindsay Jr., Walter Lee
Group Art Unit: 2812

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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3-206

Date


Stacy Seglak

Mail Stop RCE
Commissioner for Patents
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TRANSMITTAL LETTER

Sir:

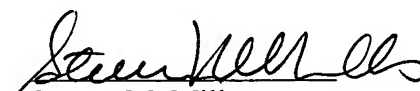
Enclosed herewith for filing in the above-identified patent application please find the following listed items:

1. Petition for Extension of Time Under 37 C.F.R. § 1.136(a);
2. Request for Continued Examination (RCE) Transmittal;
3. Check in the amount of \$910.00 to cover Filing Fee and One-Month Extension of Time; and
4. Return Postcard

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

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